

PATENT Customer No. 22,852 Attorney Docket No. 04329.2613

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re A	pplication of:		TEC
Katsuhiko HIEDA		Group Art Unit: 2814	RECENTECHNOLOGY
Serial No.: 09/916,509		Examiner: T. Le	15 ENE
Filed:	July 30, 2001		CENTER PINE PINE PINE PINE PINE PINE PINE PINE
For:	SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING THE SAME		2800
Comm	issioner for Patents		

Washington, DC 20231

Sir:

TRANSMITTAL LETTER

Enclosed is a reply to the Office Action of March 11, 2002. The items checked below are appropriate:

Applicant hereby petitions for a month extension of time to respond to the above Office Action. The fee of \$ for the Extension is enclosed.

The claims are calculated below:

	Claims Remaining After Amendment		Highest Number Previously Paid	Present Extra	Rate	Ac	iditional Fee
Total	45	-	43	2	x \$ 18	\$	36.00
Indep.	19	-	19	0	x \$ 84		0.00
First	☐ First Presentation of Multiple Dep. Claim(s) +\$280						
		•	<u> </u>		Subtotal	\$	36.00
Reduction by ½ if small entity							0.00
				 -	TOTAL	\$	36.00

- A fee of \$36 to cover the cost of the additional claims added by this reply is enclosed.
- A fee of \$ to cover is enclosed.
- A check for \$36 to cover the above fee is enclosed.

FINNEGAN HENDERSON FARABOW GARRETT& DUNNER LLP

1300 I Street, NW Washington, DC 20005 202.408.4000 Fax 202.408.4400 www.finnegan.com Please grant any extensions of time required to enter this response and charge any additional required fees to our deposit account 06-0916.

Dated: April 11, 2002

By: Bryan S. Latham Reg. No. 49,085

FINNEGAN HENDERSON FARABOW GARRETT& DUNNER LLP

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In re Application of: Katsuhiko HIEDA Group Art Unit: 2814 Examiner: T. Le Serial No.: 09/916,509

Filed: July 30, 2001

SEMICONDUCTOR DEVICE AND

METHOD FOR

MANUFACTURING THE SAME

Assistant Commissioner for Patents Washington, DC 20231

Sir:

For:

04/12/2002 MGEBREM1

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AMENDMENT AND RESPONSE TO **ELECTION REQUIREMENT**

Please amend the application as follows:

IN THE CLAIMS:

Please amend claim 20 and add new claims 44 and 45 as follows:

20. (Amended) The method according to claim 19, wherein the convex semiconductor layer is formed with the epitaxial growth method.

FINNEGAN **HENDERSON** FARABOW

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--44. (New) The device according to claim 1, wherein a position of a deepest GARRETT& portion of the gate electrode is deeper than a position of a deepest portion of the source DUNNER LLP region and a position of a deepest portion of the drain region.